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IC4201 Hi-Fi Audio Process IC Block Diagram (VR632HF/VR692HF)

System Control Block Diagram

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IC2601 Cylinder Motor Drive IC Block Diagram

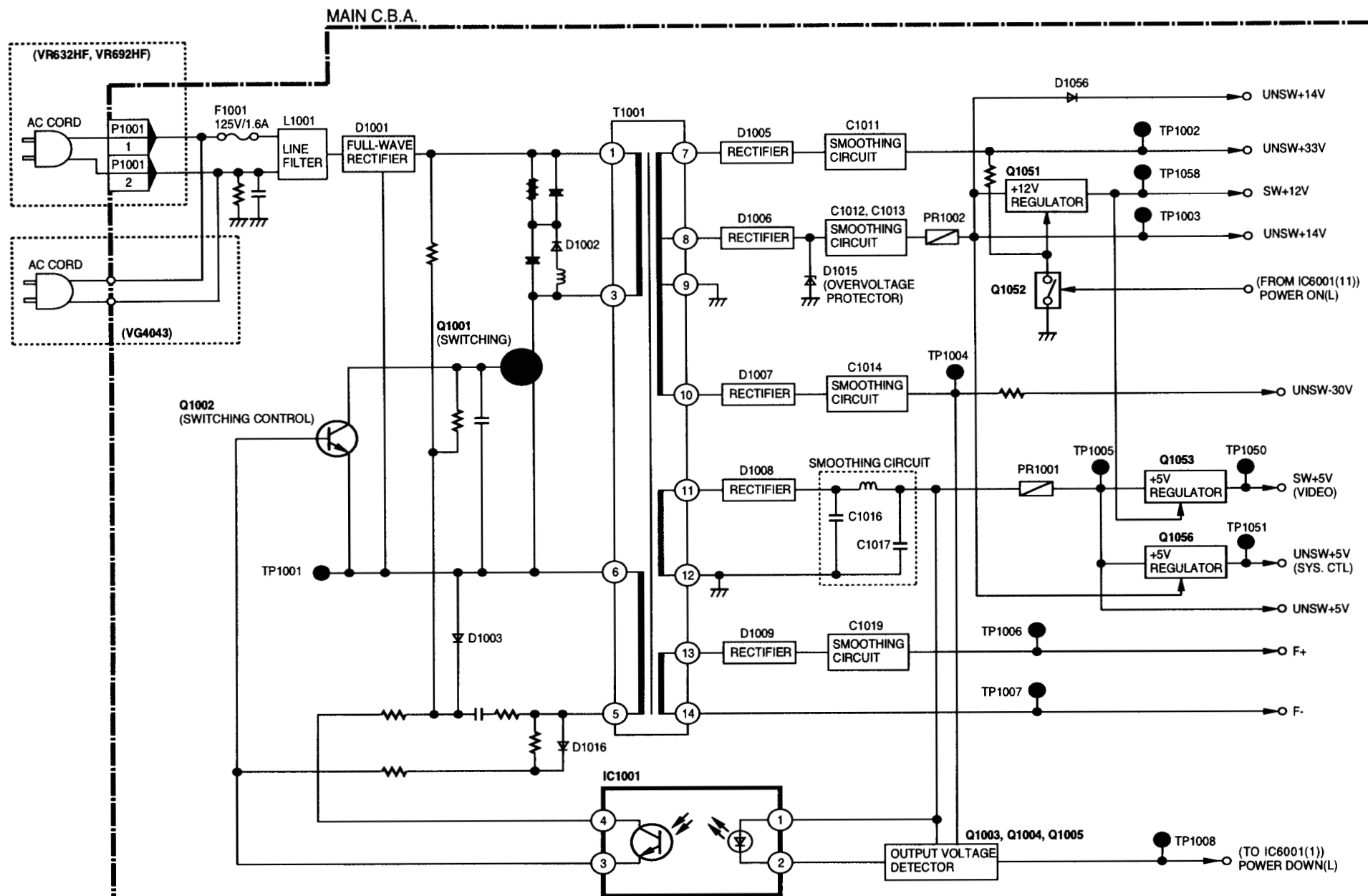
Advance Block Diagram (VR692HF)

Operation Block Diagram (VR632HF/VG4043)

Operation Block Diagram (VR692HF)

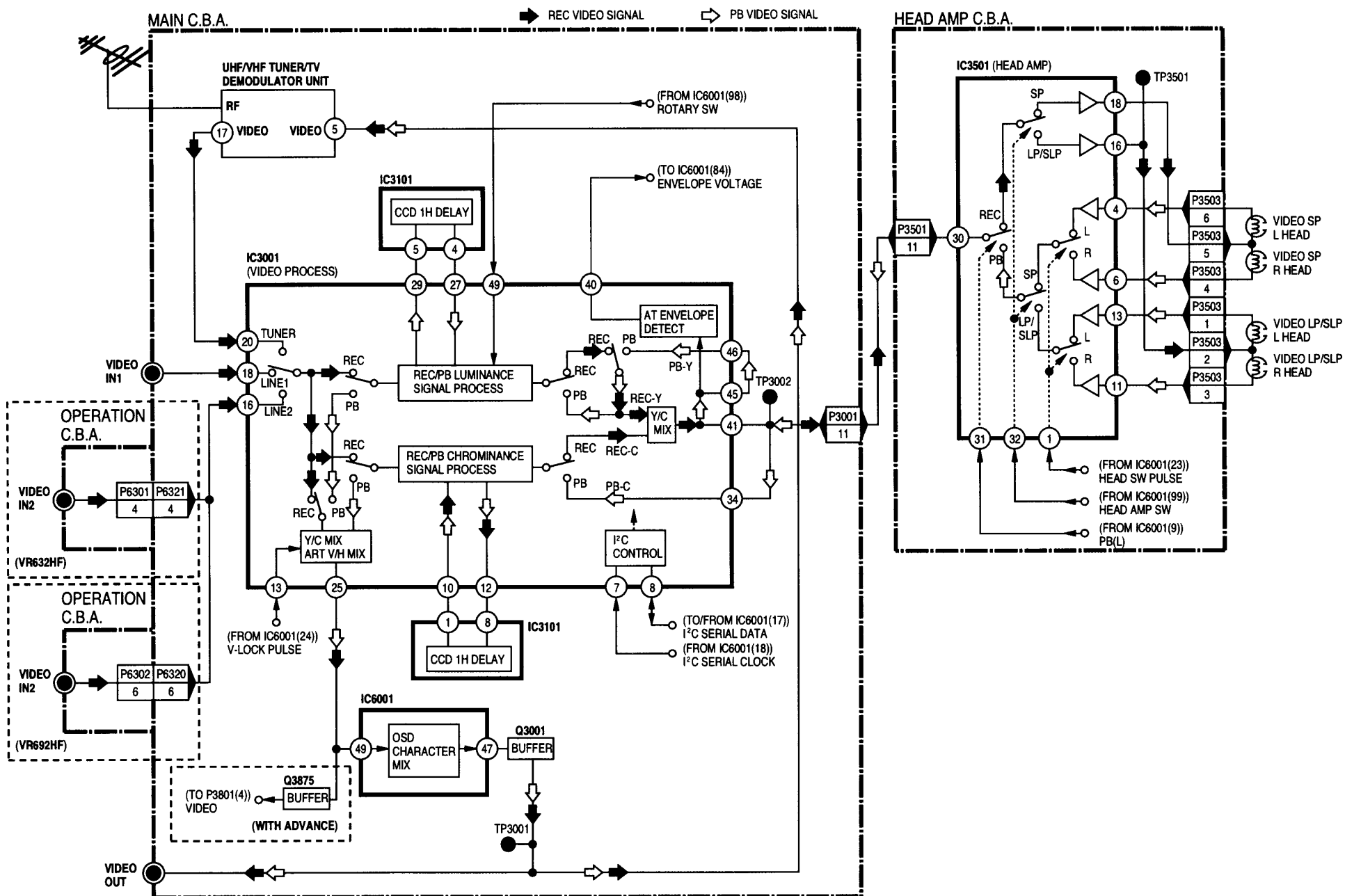
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Power Supply Block Diagram

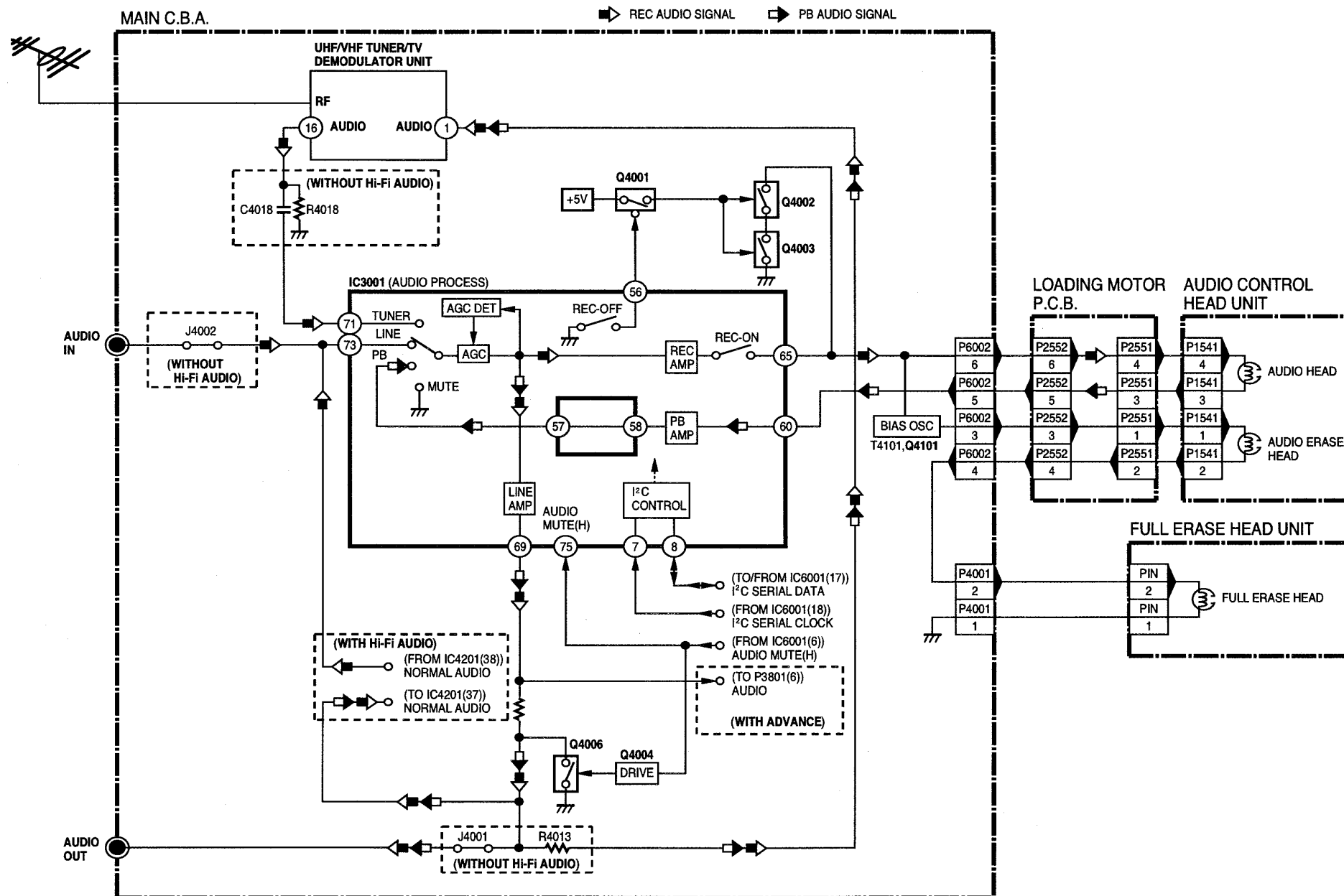
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Video Signal Path Block Diagram

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Audio Signal Path Block Diagram

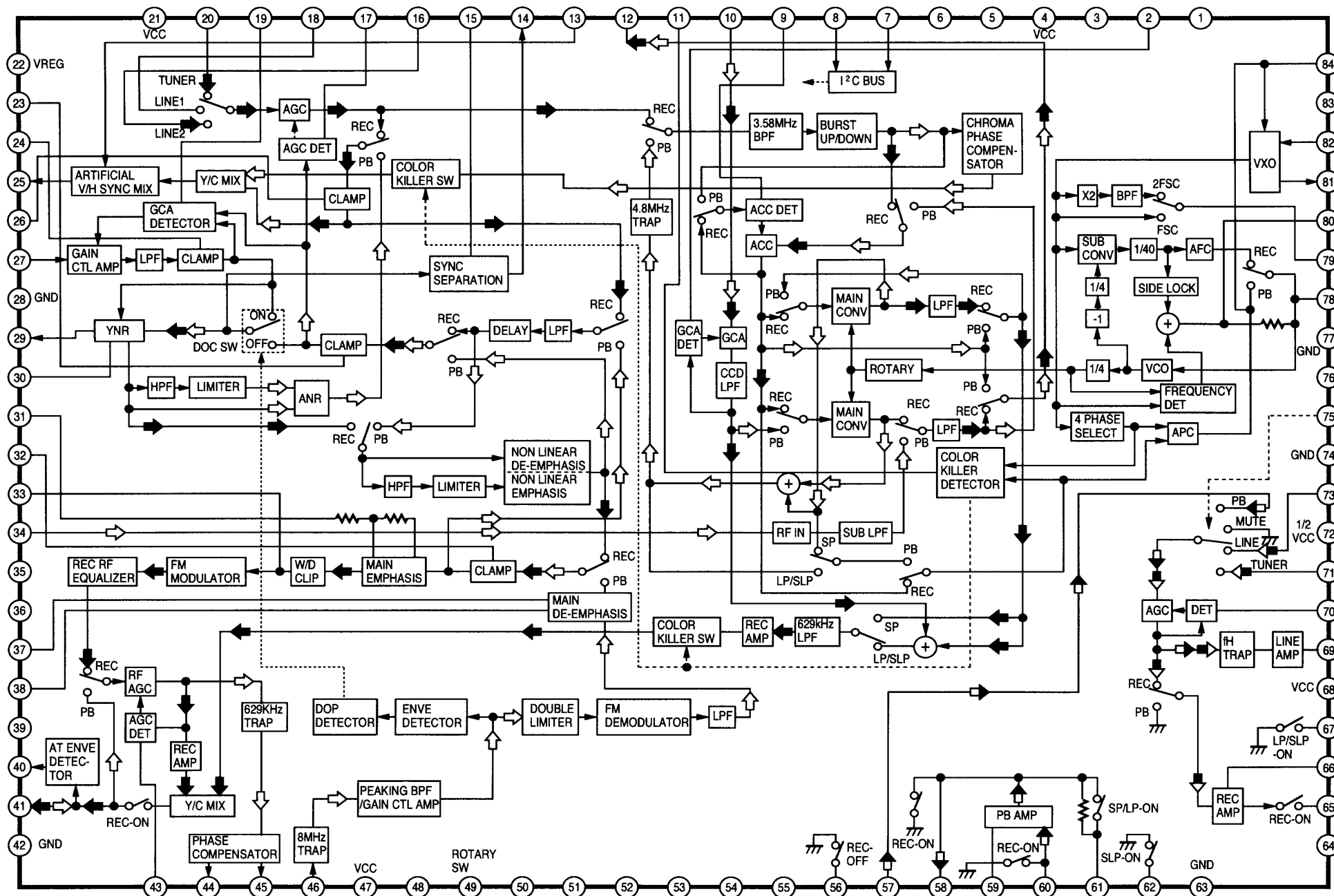


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IC3001 Video/Audio Process IC-Block Diagram, AN3476FBP

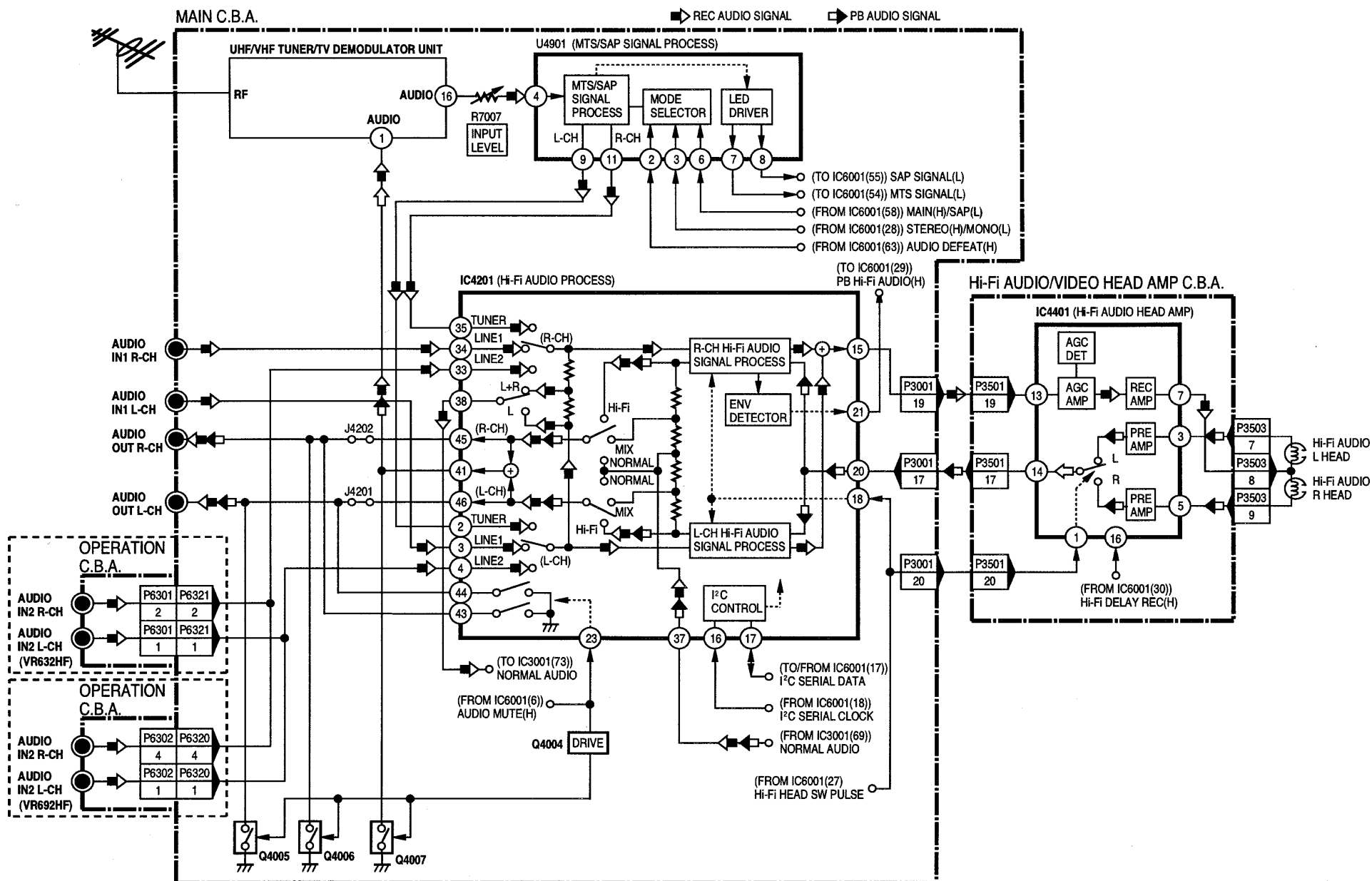
REC VIDEO SIGNAL PB VIDEO SIGNAL REC AUDIO SIGNAL PB AUDIO SIGNAL



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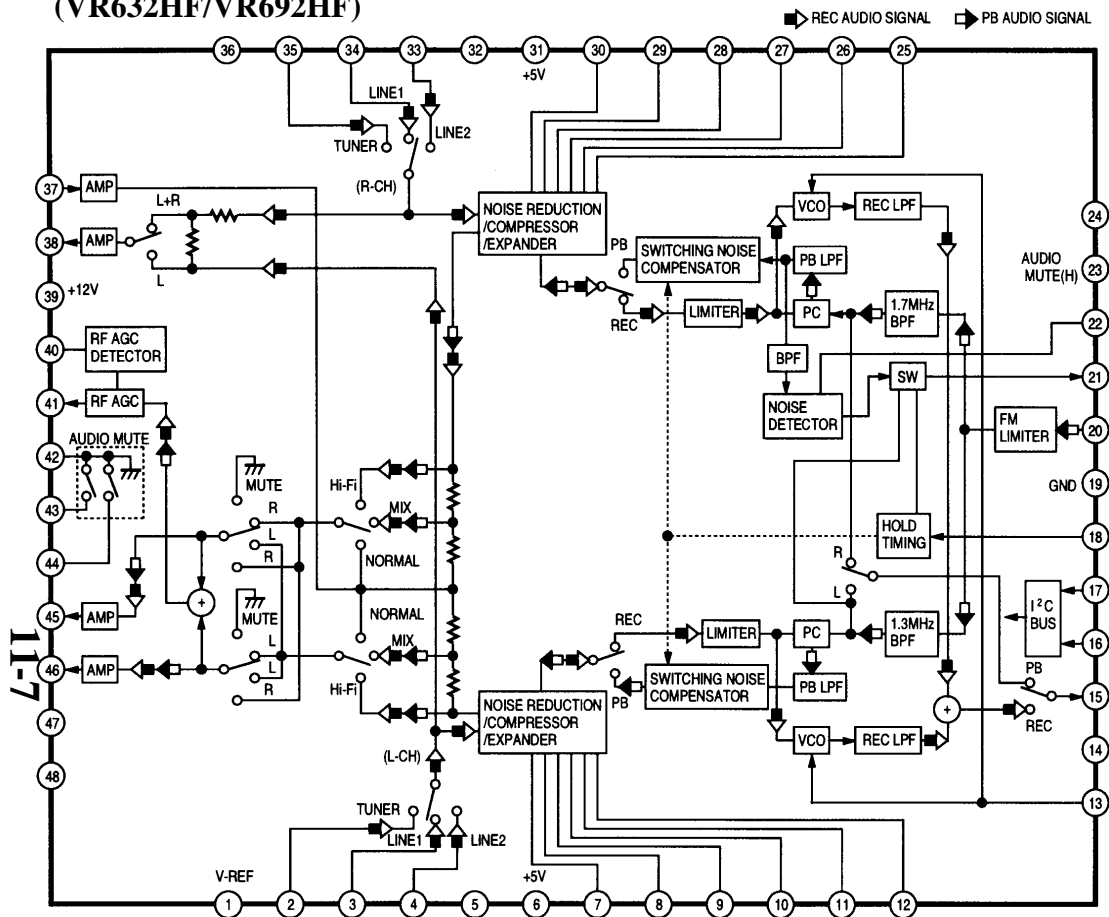
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Hi-Fi Audio Signal Path Block Diagram (VR632HF/VR692HF)



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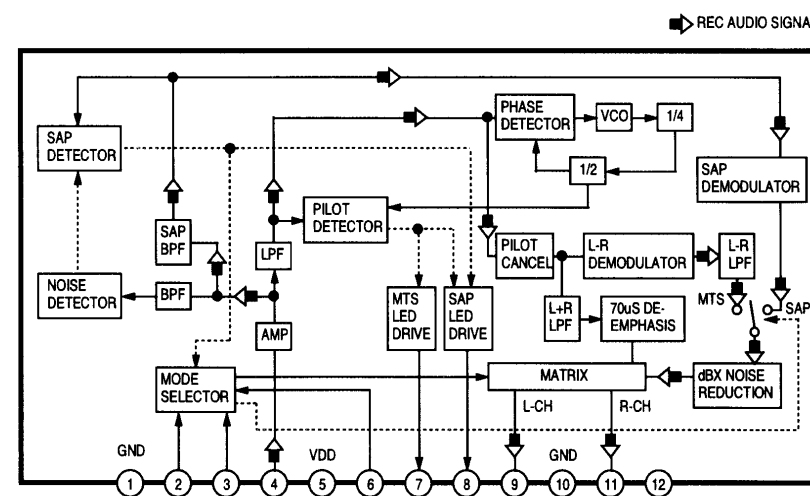
IC4201 Hi-Fi Audio Process IC-Block Diagram, AN3962FB-V (VR632HF/VR692HF)



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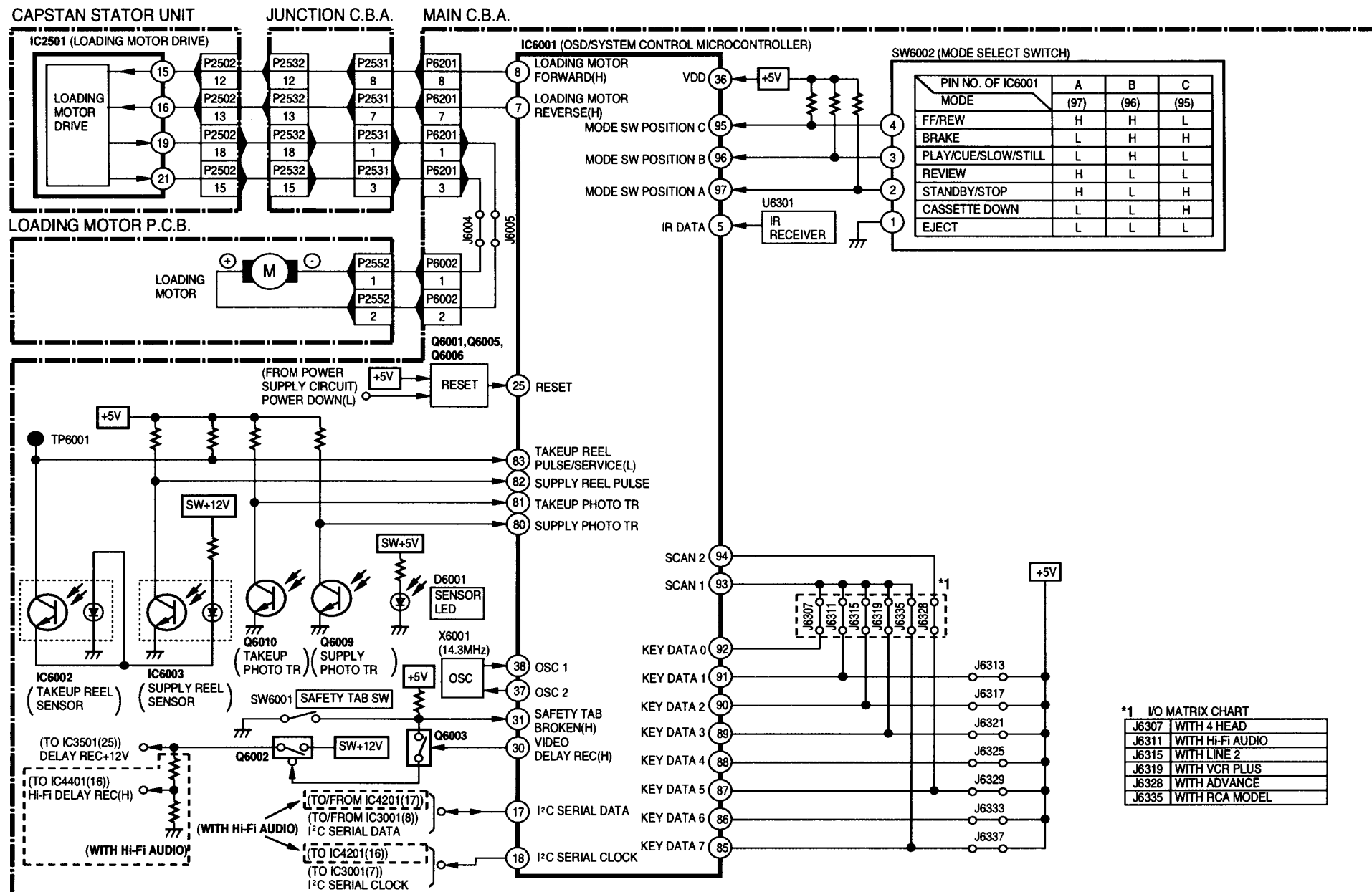
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U4901 MTS/SAP Audio Process IC-Block Diagram, VCRS0215

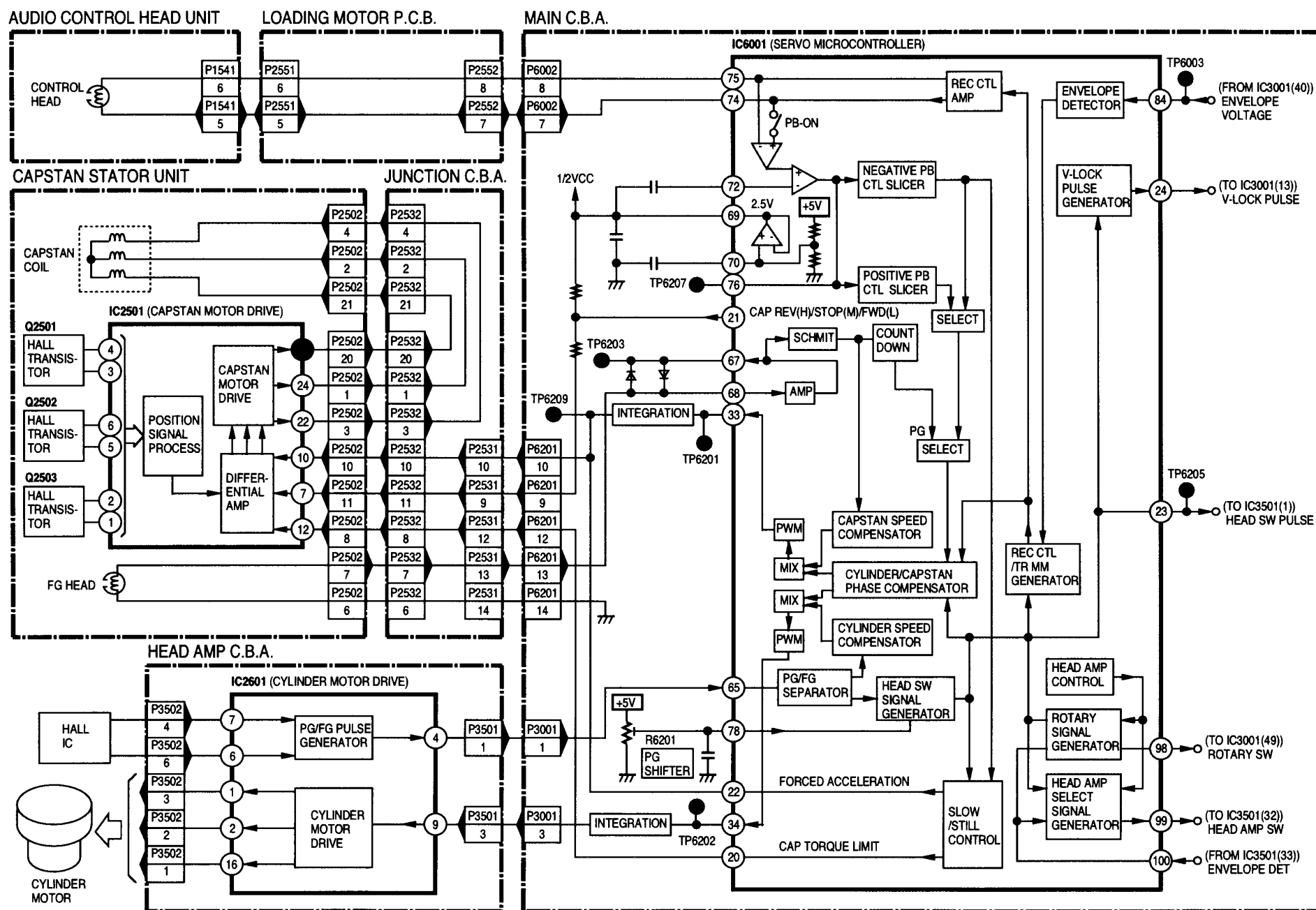


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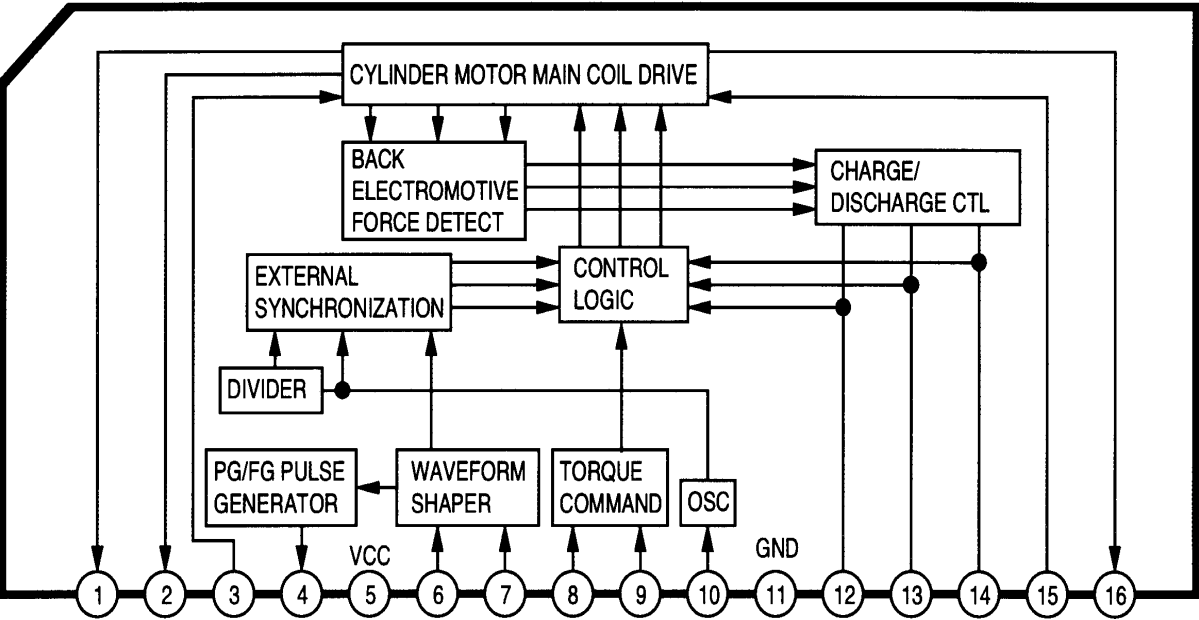
System Control Block Diagram



Servo Block Diagram



IC2601 CYLINDER MOTOR DRIVE IC-Block Diagram, AN3809K



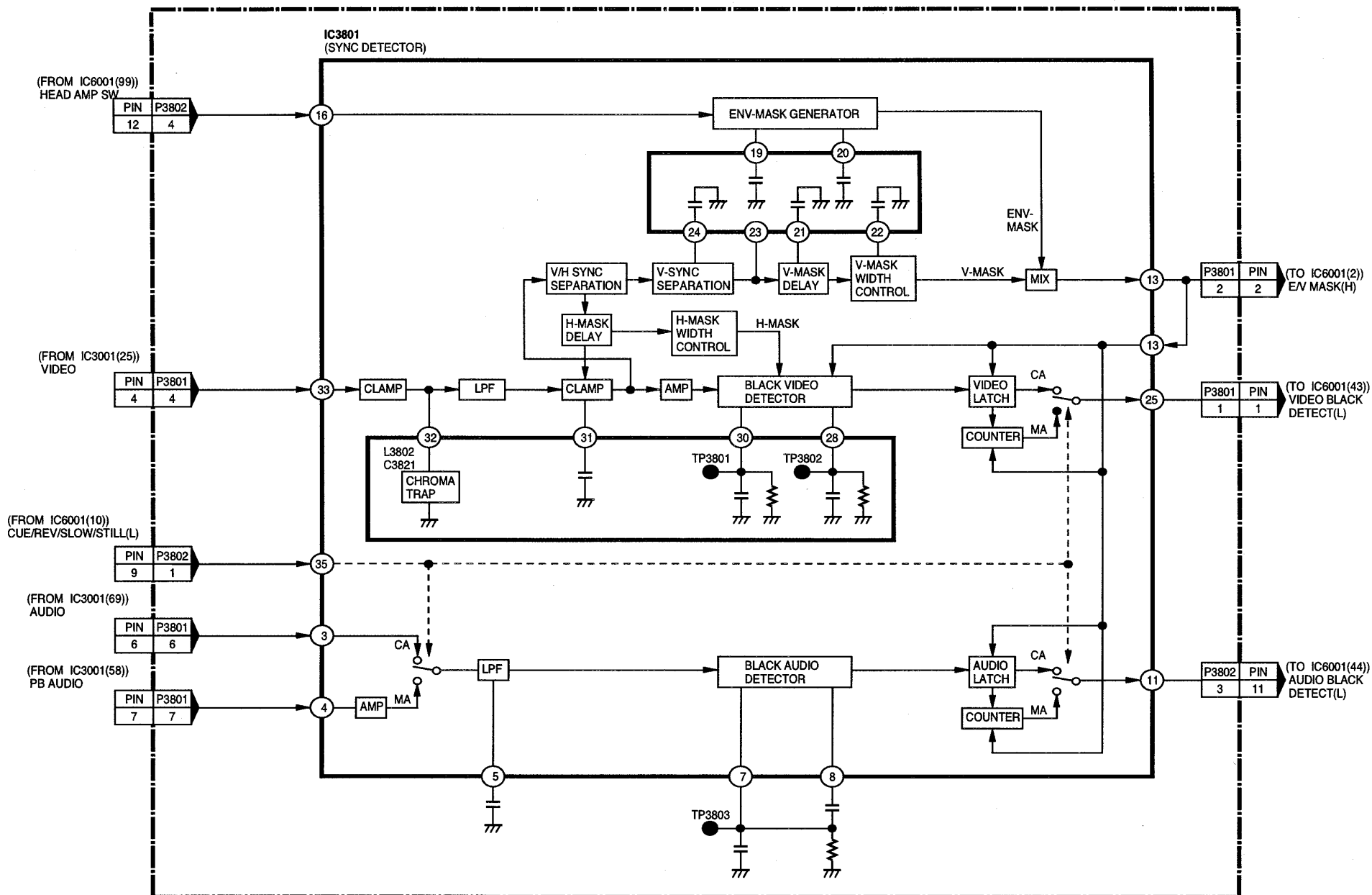
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Advance Block Diagram (VR692HF)

ADVANCE C.B.A.

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VR632HF/VR692HF/VC4043



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Operation Block Diagram (VR632HF/VG4043)

MAIN C.B.A.

VG4043

OUT IN	SCAN 0 PIN 18	SCAN 1 PIN 19	SCAN 2 PIN 20
DATA 0 PIN 9	—	PLAY/ PAUSE	FF /CUE
DATA 1 PIN 10	—	CH DOWN	TV /VCR
DATA 2 PIN 11	STOP /EJECT	POWER	REW /REV
DATA 3 PIN 12	CH UP	—	REC /TIME

VR632HF

OUT IN	SCAN 0 PIN 18	SCAN 1 PIN 19	SCAN 2 PIN 20
DATA 0 PIN 9	REC /TIME	PLAY/ PAUSE	FF /CUE
DATA 1 PIN 10	CH UP	STOP /EJECT	CH DOWN
DATA 2 PIN 11	—	POWER	REW /REV
DATA 3 PIN 12	—	—	TV /VCR

IN

OUT

DISPLAY TUBE

FIP

GRID

SEGMENT

IC6301 (INDICATOR PANEL CONTROL)

9 KEY DATA 0

12 KEY DATA 3

18 SCAN 0

19 SCAN 1

20 SCAN 2

3 GRID 1G

6 GRID 4G

44 SEGMENT j

41 SEGMENT g

38 SEGMENT f

33 SEGMENT a

VDD 39

+5V

SERIAL CLOCK 26 (FROM IC6001 (14))
SERIAL CLOCKSERIAL DATA 0 24 (FROM IC6001 (16))
SERIAL DATA 0SERIAL DATA 1 25 (TO IC6001 (15))
SERIAL DATA 1IC6301 CS(L) 22 (FROM IC6001 (60))
IC6301 CS(L)RESET(L) 15 (FROM IC6001(59))
IC6301 RESET(L)X6301
(8MHZ)

OSC

X IN 13

X OUT 14

5Vp-p

OPERATION C.B.A.

