# AUDIO INTERFACE SCHEMATIC

221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240

A B C D E F G H I J K L

ALL INTEGRATED CIRCUITS AND MANY OTHER SEMICONDUCTORS ARE ELECTROSTATICALLY SENSITIVE AND REQUIRE SPECIAL HANDLING TECHNIQUES DESCRIBED UNDER "ELECTROSTATICALLY SENSITIVE (ES) DEVICES" IN THE SAFETY AND SERVICING PRECAUTIONS PUBLICATION.

PRODUCT SAFETY NOTE

COMPONENTS WITH A ⚠ HAVE SPECIAL CHARACTERISTICS IMPORTANT TO SAFETY. BEFORE REPLACING ANY OF THESE COMPONENTS READ CAREFULLY THE PRODUCT SAFETY NOTICE IN THIS SERVICE DATA. DO NOT DEGRADE THE SAFETY OF THE SET THROUGH IMPROPER SERVICING.

SCHEMATIC NOTES
1. RESISTANCE VALUES ARE IN OHMS; K=X1000, Meg=X1,000,000.
   RESISTORS ARE 1/4W 5%, EXCEPT WHERE INDICATED.
   [ ] INDICATES FLAT MAXI-CHIP COMPONENT. 1/8W EXCEPT WHERE INDICATED.
   / \ INDICATES FLAT MINI-CHIP COMPONENT. 1/16W EXCEPT WHERE INDICATED.
2. CAPACITANCE VALUES 1.0 AND ABOVE ARE IN pF. VALUES BELOW 1.0 ARE IN Fo EXCEPT WHERE INDICATED. VOLTAGE RATING IS 50V EXCEPT WHERE INDICATED.
   [ ] INDICATES FLAT MAXI-CHIP COMPONENT.
   / \ INDICATES FLAT MINI-CHIP COMPONENT.
3. ⏚ DENOTES CHASSIS GROUND. ⊛ INDICATES CONNECTION VIA POINT-TO-POINT WIRE.
   ⌒ -INDICATES JUMPER WIRE.
   ⊶ -INDICATES ZERO OHM CHIP COMPONENT.

FROM AVR

U11501
R11511 100R0 0.5V → 9 A0
+12VrIOA <223-E> → +5V_BUS SOURCE → R11501 560R0 250M0W → 4.9V 1 VDD
CR11501 5.1V, CR11502, R11502 100K0 → 4.9V 5 RESET
C11501 220N0 16.0V, C11502 220N0 16.0V
R11503 100R0 → 4.6V 3 DATA_IN
R11505 100R0 → 4.8V 4 CLOCK
R11507 1K0 ALT, R11508 1K0 → +5V_BUS <229-A> → 0V 6 ADDR3N
R11509 1K0 ALT, R11510 1K0 → +5V_BUS <229-A> → 0V 7 ADDR2N
8 GND
16 A7 5.0V → R11519 100R0 → R11520 10K0 → +5V_BUS <229-A> → SUB_DET → TO SUB_DET
15 A6 0V → R11518 10K0 → +5V_BUS <229-A> → PA_MUTE → TO PA_MUTE
14 A5 NC
13 A4 1.7V → TRUS → TO TRUS
10 A1 NC
12 A3 0V → R11515 100K0 → +5V_BUS <229-A>; R11516 10K0 → 4.3V → Q11501 (5.0V, +5V_BUS <229-A>, R11517 100R0 → SUB_CONT → TO SUB_CONT; R11514 1K0)
11 A2 0V → R11521 1K0 → R11522 4K7 → +5V_BUS <229-A> → FAV_SPKR_MUTE → TO FAV_SPKR_MUTE

J11501
1 +12VrIOA SOURCE
2
3
4 NC
5
6
7
8
9
10
11
12

J11501 FROM J26905

+12VrIOA <223-E> → R11556 39K2 1% → R11550 47R0 250M0W → C11551 47N0 50.0V, C11550 10uT 63.0V

U11551
8
C11553 1u0 100.0V, R11553 33K0 → 6.0V 3 (+)
C11554 1u0 100.0V, R11555 33K0 → 6.0V 2 (−) → 1 6.0V → L_AUD → TO L_AUD
R11554 39K2 1% → C11555 1u0 100.0V, R11557 33K0 → 6.0V 5 (+)
C11556 1N0, C11557 1N0
R11559 33K0 → 6.0V 6 (−) → 7 6.0V → R_AUD → TO R_AUD
4
R11558 39K2 1%
R11560 39K2 1%
R11551 10K0, C11552 100u0 35.0V, R11552 10K0 → +12VrIOA <223-E>

A000

FAV_HEAD_R → TO FAV_HEAD_R
FAV_HEAD_L → TO FAV_HEAD_L

FROM FAV_HEAD_COM → FAV_HEAD_COM
FROM AUD_L → AUD_L
FROM AUD_COM → AUD_COM
FROM AUD_R → AUD_R

J26905
12 FAV_HEAD_COM
11 FAV_HEAD_L
10 FAV_HEAD_R
9 AUD_R
8
7
6 AUD_COM
5 AUD_L
4 NC
3 DACQ_I2C_CLK <538-I>
2 DACQ_I2C_DAT <539-I>
1 +12VrHO <40-I>
→ J26905 TO J11501


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